



PATENT

2112  
41IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Kenneth Kotlowski

Title: SPLIT TRANSACTIONAL UNIDIRECTIONAL BUS ARCHITECTURE  
AND METHOD OF OPERATION

Application No.: 09/845,504

Filed: 04-30-2001

Examiner: Paul R. Myers

Group Art Unit: 2112

Atty. Docket No.: 1458-P04919

Conf. No.: 2092

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JUN 10 2004

COMMISSIONER FOR PATENTS  
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Technology Center 2100

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Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the undersigned brings the patents, publications, applications or other information identified in the attached:

- ☒ Form(s) PTO/SB/08A and/or PTO/SB/08B  
☐ Other: n/a

to the Examiner's attention in the above-identified application.

An electronic IDS has been submitted on this day which is not complete for the following reasons:

- ☒ foreign patent documents and non-patent literature are included herewith  
☐ the earlier electronic submission includes only 50 U.S. patents and U.S. patent application publications and more than one submission listing U.S. patents and/or U.S. patent applications is needed to file a complete IDS.

## CERTIFICATE OF TRANSMISSION/MAILING

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Signature

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Citation of such information shall not be construed as:

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3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

For each item of information listed that is not in the English language, the undersigned has provided a concise explanation of the relevance through (i) an English language abstract, (ii) an English language equivalent application, or (iii) if cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action that indicates the degree of relevance found by the foreign office.

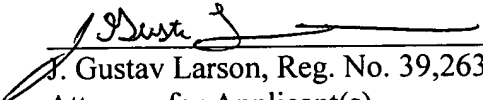
#### FEE AUTHORIZATION

- ☒ A fee is required under 37 CFR 1.17(p). As the fee was included in an earlier electronic submission of the same date, we request that the one submitted fee be accepted for this and any subsequent submission which subsequent submissions (electronic or paper) are received by the Office on the same date as the first electronic submission with which the fee was included.
- ☐ This Information Disclosure Statement is filed within three months of the filing date of a national application other than a continued prosecution application under § 1.53(d) or within three months of entry of the national stage as set forth in § 1.491 in an international application. Therefore, no fee is required.
- ☐ The undersigned believes that this Information Disclosure Statement is being filed before the mailing date of a first Office action on the merits or before the mailing date of a first Office action after the filing of a request for continued examination under § 1.114. Therefore, no fee is believed required.

If however, this Information Disclosure Statement is determined by the USPTO to be filed after the period specified in § 1.97(b), the undersigned hereby authorizes the Commissioner to charge the fee set forth in § 1.17(p) to Deposit Account No. 01-0365.

Respectfully submitted,

6-2-04  
Date

  
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# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Substitute for form 1449A/PTO

Sheet 1 of 1

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Complete if Known

Application Number: 09/845,504

Filing Date: 04-30-2001

First Named Inventor: Kenneth Kotlowski

Group Art Unit: 2112

Examiner Name: Paul R. Myers

Attorney Docket Number: 1458-P04919

## U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. 1	U.S. Patent Document Number	Kind Code 2 (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	A1					
	A2					
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## FOREIGN PATENT DOCUMENTS

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	A11	JP	410285011	A	Aihara	10-23-1998	Abstract only	<input checked="" type="checkbox"/>
	A12	DE	19949144	C1	Goetz	01-01-2001	Abstract only	<input checked="" type="checkbox"/>
	A13							<input type="checkbox"/>

## PUBLICATIONS

Examiner Initials *	Cite No. 1	Title of Publication	Date of Publication of Cited Document MM-DD-YYYY
	A14	Calder, B and Grunwald, D., "Fast and Accurate Instruction Fetch and Branch Prediction," Computer Architecture, Proceedings the 2nd Annual International Symposium on, 1 pp.	4/18-21/1994
	A15	John P. Uyemura, "CMOS Logic Circuit Design," Kluwer Academic Publishers, Boston, MA; ISBN 0-7923-8452-0, pp. 144 & 145	2001
	A16	Wee-Seng Soh and Kim, H.S., "Dynamic bandwidth reservation in hierarchical wireless ATM networks using GPS-based prediction," IEEE VTS 50th Vehicular Technology Conference, Amsterdam, Netherlands, on pp. 428-432 Vol. 1, [online] Retrieved from IEEE Xplore, Abstract, 2 pages	9-19-1999
	A17	Polze, A. et al., "Predictable Network Computing," Proceedings of the 17th International Conference on Distributed Computing Systems, Baltimore, MD, on pp. 423-431, [online] Retrieved from IEEE Xplore, Abstract, 2 pages	5-27-1997

Examiner Signature		Date Considered	
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